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are OFF when  $V_{GS} = 0$ . In analog circuits, sometimes the source (and even the body terminal) may not be at

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noise The input offset voltage of a comparator is the input voltage at which its output changes

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This is one of very few books dedicated entirely to MOS analog circuits. We will use some of the material in chapter 8 on the topic of CMOS op-amp design. D.A. Johns, K. Martin, Analog Integrated Circuit Design, J. Wiley & Sons, 1997.

This is also a very good book that covers a wide range of topics dealing with



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