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This manual documents the Microcontroller profile of version 7 of the ARM® Architecture, the ARMv7-M architecture profile. For short definitions of all

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the ARMv7 profiles see  
About the ARMv7  
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architecture, and  
architecture profiles on  
page A1-20. The  
manual has the  
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## **ARM architecture - Wikipedia**

The architectures are binary instruction upward compatible from ARMv6-M to ARMv7-M to ARMv7E-M. Binary instructions available for the Cortex-M0 / Cortex-M0+ / Cortex-M1 can execute



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without modification on  
the Cortex-M3 / Cortex-  
M4 / Cortex-M7.

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### Architecture

For consistency with other ARM Architecture Reference Manuals, the term Modified Virtual Address (MVA) is used throughout this manual, even though ARMv7-M has no concept of virtual addresses (VAs). For the ARMv7-M architecture profile in all cases the MVA, VA, and PA have the same value.

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### Architecture

from the armv7-m reference manual that the difference between armv7-m and armv7e-m is of the DSP chip i.e. armv7e-m will have a DSP chip. As the k70 has a DSP chip, therefore I'm able to conclude that I have the armv7e-m architecture. However, I'm unable to figure out the difference between armv7e-m and armv7e-m-pic?

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## **Documentation - Arm Developer**

ARMv7-M and ARMv7E-M architectures always include divide instructions. ARMv7-R architecture always includes divide instructions in the Thumb instruction set, but optionally in its 32-bit instruction set. ARMv7-A architecture optionally includes the divide instructions.

**ARMv7-M**

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